



Hardware

Board Specifications

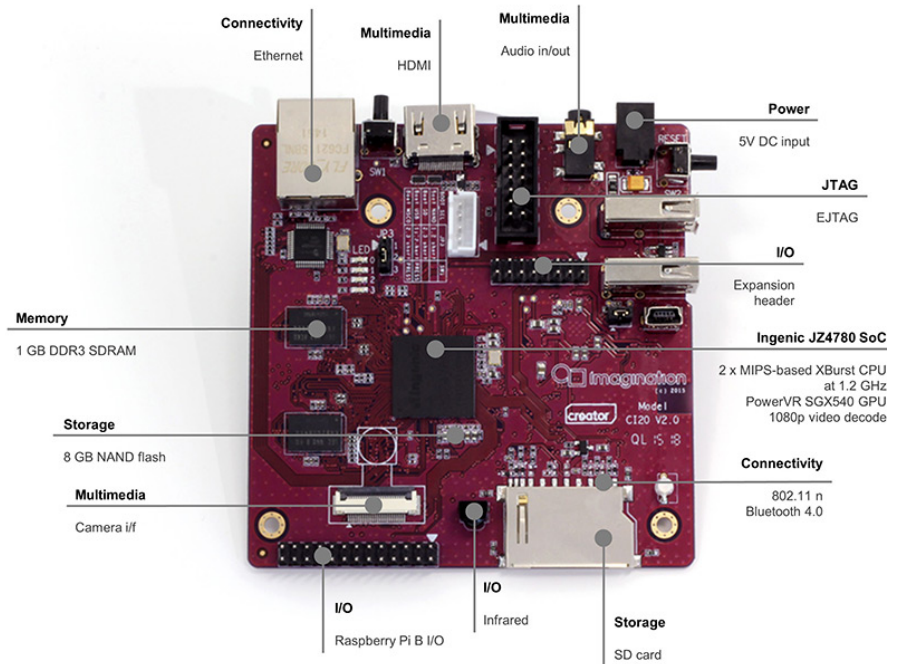
Expansion Headers

Creator Ci20

Welcome to the Creator Ci20 documentation. The Ci20 development board explores the power of MIPS archt running Linux Debian 8 on a dual 1.2GHz microcontroller.

With 1GB of RAM Ci20 integrates Wi-Fi and Bluetooth 4.0 connectivity, transforming this board in a powerful I platform. The Ci20 board incorporates several peripherals: GPIO, SPI, I2C, ADC and UART, allowing great devel flexibility for any project.

Board Specifications



Feature	Details
SoC	Ingenic JZ4780
CPU	Dual 1.2GHz XBurst MIPS32 Little Endian
GPU	PowerVR SGX540
RAM	1GB DDR3
Storage	8GB NAND Flash
Connectivity	10/100 Ethernet, 802.11n Wi-Fi, Bluetooth 4.0
GPIO	25-pin headers
Ports	HDMI, 3.5 mm Audio connector, 2xUSB, SD card, Camera(ITU645)
Peripherals	GPIO, SPI, I2C, ADC, PWM, UART, JTAG, Infrared



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Feature Details

Power 5V

- Board Schematics (http://mipscreator.imgtec.com/Ci20/hardware/board/ci20_jz4780_v2.0.pdf)
- Board Layout (http://mipscreator.imgtec.com/Ci20/hardware/board/Ci20_V1.0_layout_with%20part%20number.pdf)

Expansion Headers

The Ci20 board has 4 expansion headers: Primary, Secondary, UART and EJTAG.

Primary Expansion Header

The following image shows the pinout of the 26 pin primary header. It is seen an arrow pointing to the pin number seen also on the Ci20 board, for easy pinout matching.

GPIO No							GPIO No
	3.3V PWR	1	▶		2	5V PWR	
PD30(126)	I2 C1 SDA	3			4	5V PWR	
PD31(127)	I2 C1 SCL	5			6	GND	
PD28(124)	GPIO 1	7			8	UART0 TX	PF3(163)
	GND	9			10	UART0 RX	PF0(160)
PD26(122)	GPIO 2	11			12	-	
PD27(123)	GPIO 3	13			14	GND	
PD29(125)	GPIO 4	15			16	GPIO 5	PF1(161)
	3.3V PWR	17			18	GPIO 6	PF2(162)
PE17(145)	SSIO DT	19			20	GND	
PE14(142)	SSIO DR	21			22	GPIO 7	PE8(136)
PE145(143)	SSIO CLK	23			24	SSIO CEO	PE16(144)
	GND	25			26	SSIO CE1	PE18(146)

For more information about the expansion headers:

- Primary Expansion Header (http://elinux.org/Ci20_Hardware#Primary_expansion_header)
- Secondary Expansion Header (http://elinux.org/Ci20_Hardware#Secondary_expansion_header)
- UART (http://elinux.org/Ci20_Hardware#Dedicated_UART_header)
- EJTAG (http://elinux.org/Ci20_Hardware#EJTAG)